

Generalized Coupled Interconnect Transfer Function and High-Speed Signal Simulations

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Abstract—A new expression for the coupled interconnect system transfer function has been derived under general linear generator and uncoupled load conditions, i.e., without any restrictions in circuit load impedance. High-speed signals on coupled interconnects have been simulated using this transfer function. The simulation uses generalized interconnect circuit model parameters in which all line parameters are frequency dependent. The validity of the interconnect circuit parameters was confirmed previously using s -parameter measurements. High-speed signal simulation using this novel interconnect transfer function has been verified with time-domain measurements using an HP54121T high-speed sampling oscilloscope. This work accurately predicts coupled interconnect circuit responses. With this transfer function, signal integrity problems of high-performance VLSI circuits can be predicted in the design stage.

I. INTRODUCTION

WITH the rapid increase of integration level and speed, IC interconnect becomes one of the important limiting factors of today's VLSI circuit design performance. The chip area occupied by interconnect, as well as the average length of the interconnect are increasing. Furthermore, high-speed operation in high-density circuits can cause unanticipated signal problems. In order to design high-density and high-speed VLSI circuits, accurate knowledge of the electrical characteristics of the interconnect is essential [1]–[6]. Then signal delay, transient, and crosstalk problems can be avoided.

The conventional RC interconnect circuit model is based on simplifying assumptions that are inaccurate at high frequencies. The model accounts only for capacitive coupling, assumes interconnect line parameters are constant with frequency, neglects dielectric loss, and restricts circuit loading conditions. Many methods that predict exact interconnect circuit response have been constructed [2], [6]–[9]. The coupled interconnect line transfer functions under unmatched, but special case terminal conditions have been reported in many paper [3], [7]–[10]. However, these interconnect signal simulations are not accurate for submicron VLSI-based high-speed circuits because some assumptions were made that are the same as those of the RC models.

A new expression for the n -coupled interconnect system transfer function has been derived under general linear genera-

tor and uncoupled load conditions, i.e., without any restrictions in circuit load impedance or circuit generator impedance. Under these generalized conditions, high-speed signals on coupled interconnects have been simulated. In this simulation, generalized interconnect circuit parameters (all line parameters are frequency dependent) have been used. The line parameter models are based on high-frequency s -parameter measurements [11]. The validity of the interconnect model parameters was verified in previous work [11] with low-frequency and s -parameter measurement techniques. Interconnect simulations using the generalized interconnect transfer function has been verified with time-domain measurements using an HP54121T high-speed sampling oscilloscope. The simulations and measurements show good agreement.

For practical interconnect circuit simulation, it is virtually impossible to consider all high-frequency-related effects such as frequency dependence of the line parameters, substrate coupling effects, 3-D effects, nonlinear loading effects, etc., at one time. This paper's contribution is a theoretical analysis of n -coupled line interconnect with frequency-variant line parameters. A high-speed experimental measurement of on-chip IC interconnect coupled line response demonstrates the accuracy of the analysis. Moreover, the generalized transfer function in this analysis lends itself to using experimental data as input. Thus, an engineer can use the generalized transfer function and the results in [11] to generate comparisons between existing interconnect circuit simulation models and high-frequency s -parameter measurement-based modeling and simulation.

The general guidelines for interconnect circuit modeling are under continual revision with increasing IC transistor performance and continual reduction in IC interconnect linewidth and spacing. There exists a difficult tradeoff among interconnect model accuracy, complexity, and efficiency. This theoretical analysis can be used to evaluate the accuracy of simplified interconnect circuit models for high-speed circuit simulation by providing accurate estimates of signal delay, jitter, and crosstalk.

II. n -COUPLED LINE INTERCONNECT TRANSFER FUNCTION

To perform transient simulations of coupled interconnect circuits, it is necessary to have an accurate circuit transfer function. This section develops a new circuit transfer function for multiconductor coupled interconnects with frequency-dependent parameters. The load impedance, linear generator impedance, and number of lines is unrestricted.

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Electrical properties of multiconductor transmission lines can be simulated by the following matrix form representation of the Telegrapher's equations:

$$\frac{d[V(x)]}{dx} = [Z_s][I(x)] \quad (1)$$

$$\frac{d[I(x)]}{dx} = [Y_p][V(x)] \quad (2)$$

where $[V(x)]$ and $[I(x)]$ are line voltages and currents and $[Z_s]$ and $[Y_p]$ are composed of

$$[Z_s] = [R] + j\omega[L] = \begin{bmatrix} Z_{11} & \cdots & Z_{1n} \\ \vdots & \ddots & \vdots \\ Z_{n1} & \cdots & Z_{nn} \end{bmatrix} \quad (3)$$

$$[Y_p] = [G] + j\omega[C] = \begin{bmatrix} Y_{11} & \cdots & Y_{1n} \\ \vdots & \ddots & \vdots \\ Y_{n1} & \cdots & Y_{nn} \end{bmatrix} \quad (4)$$

where

$$C_{ii} = \sum_{j=1}^n c_{ij} \quad \text{and} \quad C_{ij} = -c_{ij}; \quad (5)$$

similarly,

$$G_{ii} = \sum_{j=1}^n g_{ij} \quad \text{and} \quad G_{ij} = -g_{ij}. \quad (6)$$

The propagation constant and characteristic impedance are derived by solving eigenvalue equations. The voltage eigenmatrix $[S_v]$ and current eigenmatrix $[S_i]$ are the solutions of the eigenvalue equations

$$(\gamma^2[U] - [Z_s][Y_p])[S_v] = 0 \quad (7)$$

$$[S_i] = [Z_s]^{-1}[S_v][\Lambda] \quad (8)$$

where $[U]$ is the identity matrix and $[\Lambda]$ is the diagonal matrix, $\text{diag}\{\gamma_1, \gamma_2, \dots, \gamma_n\}$. Note that the square roots of the eigenvalues are physical propagation constants.

Solving the eigenvalue equation (7) with boundary conditions of Fig. 1, the following matrix equation can be derived (9), shown at the bottom of the page, where the coupled line voltage eigenmatrix $[S_v]$ and current eigenmatrix $[S_i]$ can be readily determined and $[E(x)]$ is a diagonal matrix,

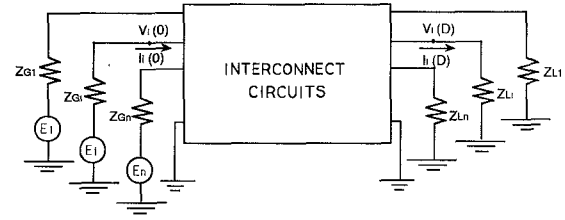


Fig. 1. Multiconductor interconnect circuits with arbitrary generator impedances and load impedances.

$\text{diag}\{e^{-\gamma_1 x}, \dots, e^{-\gamma_n x}\}$. Also, $[C_1]$ and $[C_2]$ are matrices of voltage wave magnitudes. Thus, $[C_1]$ represents port voltage magnitudes for propagation in the reverse direction, and $[C_2]$ represents port voltage magnitudes for propagation in the forward direction. In (9), the system matrix can be uniquely determined if the following are known: 1) voltage eigenmatrix and current eigenmatrix, 2) input and output impedances, and 3) propagation constant matrix and characteristic impedance matrix which require knowledge of transmission line parameter matrices $[R]$, $[L]$, $[C]$, and $[G]$. Among these, 3) is the most important because it physically represents the particular interconnect system characteristics. Quantities 1) and 2) can be readily determined from a given interconnect system configuration.

Representing (9) in a simpler linear matrix form

$$[b] = [A][X] \quad (10)$$

where (see (11), shown at the bottom of the page)

$$[X] = \begin{bmatrix} [C_1] \\ [C_2] \end{bmatrix} = \begin{bmatrix} x_1 \\ \vdots \\ x_n \\ x_{n+1} \\ \vdots \\ x_{2n} \end{bmatrix} \quad (12)$$

$$[b] = \begin{bmatrix} [E_i] \\ [0] \end{bmatrix} \quad (13)$$

where

$$[E_i] = \begin{bmatrix} 0 \\ \vdots \\ E_i \\ \vdots \\ 0 \end{bmatrix}. \quad (14)$$

$$\begin{bmatrix} [E_i] \\ [0] \end{bmatrix} = \begin{bmatrix} [S_v] + [Z_G][S_i] & [S_v] - [Z_L][S_i] \\ ([S_v] + [Z_L][S_i])[E(D)] & ([S_v] - [Z_L][S_i])[E(D)]^{-1} \end{bmatrix} \begin{bmatrix} [C_1] \\ [C_2] \end{bmatrix} \quad (9)$$

$$[A] = \begin{bmatrix} a_{11} & \cdots & a_{12n} \\ \vdots & \ddots & \vdots \\ a_{2n1} & \cdots & a_{2n2n} \end{bmatrix} = \begin{bmatrix} [S_v] + [Z_G][S_i] & [S_v] - [Z_L][S_i] \\ ([S_v] + [Z_L][S_i])[E(D)] & ([S_v] - [Z_L][S_i])[E(D)]^{-1} \end{bmatrix} \quad (11)$$

Now, a new matrix $[B^j]$ is defined as a matrix in which the j th column of matrix $[A]$ is replaced by the column matrix $[b]$, i.e.

$$[B^j] = \begin{bmatrix} a_{11} & \cdots & a_{1j-1} & 0 & a_{1j+1} & \cdots & a_{12n} \\ \vdots & & \vdots & E_i & \vdots & & \vdots \\ a_{2n1} & \cdots & a_{2nj-1} & 0 & a_{2nj+1} & \cdots & a_{2n2n} \end{bmatrix} \quad (15)$$

Thus, solving (10) with Cramer's rule yields the components of the port wave magnitudes of the system

$$x_j = \frac{E_i \Delta_{ij}}{\det(A)} \quad (16)$$

where

$$\Delta_{ij} = \text{Cof}_{ij}(B^j). \quad (17)$$

Since input signals (voltages) at the n -coupled interconnects are

$$\begin{aligned} [V] &= [S_v] \left(\begin{bmatrix} x_1 \\ \vdots \\ x_n \end{bmatrix} + \begin{bmatrix} x_{n+1} \\ \vdots \\ x_{2n} \end{bmatrix} \right) \\ &= [S_v]([C_1] + [C_2]), \end{aligned} \quad (18a)$$

the response of n -coupled interconnect is rewritten as

$$\begin{aligned} [V] &= \begin{bmatrix} s_{11} & \cdots & s_{1n} \\ \vdots & \ddots & \vdots \\ s_{n1} & \cdots & s_{nn} \end{bmatrix} \left(\begin{bmatrix} x_1 \\ \vdots \\ x_n \end{bmatrix} + \begin{bmatrix} x_{n+1} \\ \vdots \\ x_{2n} \end{bmatrix} \right) \\ &\triangleq \left(\begin{bmatrix} p_1 \\ \vdots \\ p_n \end{bmatrix} + \begin{bmatrix} q_1 \\ \vdots \\ q_n \end{bmatrix} \right) = \begin{bmatrix} p_1 + q_1 \\ \vdots \\ p_n + q_n \end{bmatrix} \end{aligned} \quad (18b)$$

where

$$\begin{cases} p_i = \sum_{j=1}^n s_{ij} x_j \\ q_i = \sum_{j=1}^n s_{ij} x_{n+j} \end{cases} \quad (19)$$

Thus

$$\begin{aligned} V_i &= p_i + q_i \\ &= \sum_{j=1}^n s_{ij} (x_j + x_{n+j}). \end{aligned} \quad (20)$$

To determine overall interconnect system response, the superposition principle is applied. Consider each independent source as follows. If E_1 is excited and other sources are zero, that is, if

$$E_1 \neq 0 \quad \text{and} \quad E_2 = E_3 = \cdots = E_n = 0,$$

then the following circuit responses can be determined:

$$\left(\frac{V_1}{E_1}, \frac{V_2}{E_1}, \dots, \frac{V_n}{E_1} \right). \quad (21a)$$

If E_2 is excited and other sources are zero, that is, if

$$E_2 \neq 0 \quad \text{and} \quad E_1 = E_3 = \cdots = E_n = 0,$$

then the following circuit responses can be determined:

$$\left(\frac{V_1}{E_2}, \frac{V_2}{E_2}, \dots, \frac{V_n}{E_2} \right). \quad (21b)$$

Similarly, extending these procedures on the n th source E_n , that is, if

$$E_n \neq 0 \quad \text{and} \quad E_1 = E_2 = \cdots = E_{n-1} = 0,$$

then all circuit responses can be determined

$$\left(\frac{V_1}{E_n}, \frac{V_2}{E_n}, \dots, \frac{V_n}{E_n} \right). \quad (21c)$$

From (21) and the superposition principle, transfer functions of each line are determined. That is, the transfer function of line 1 is

$$\begin{aligned} H_1 &= \frac{V_1}{E_1} + \frac{V_1}{E_2} + \cdots + \frac{V_1}{E_n} \\ &= V_1 \sum_{i=1}^n \left(\frac{1}{E_i} \right), \end{aligned} \quad (22a)$$

the transfer function of line 2 is

$$\begin{aligned} H_2 &= \frac{V_2}{E_1} + \frac{V_2}{E_2} + \cdots + \frac{V_2}{E_n} \\ &= V_2 \sum_{i=1}^n \left(\frac{1}{E_i} \right), \end{aligned} \quad (22b)$$

and similarly, the transfer function of line n is

$$\begin{aligned} H_n &= \frac{V_n}{E_1} + \frac{V_n}{E_2} + \cdots + \frac{V_n}{E_n} \\ &= V_n \sum_{i=1}^n \left(\frac{1}{E_i} \right). \end{aligned} \quad (22c)$$

Thus, the generalized interconnect transfer function of line j for a given n -conductor system is

$$\begin{aligned} H_j &= V_j \sum_{i=1}^n \left(\frac{1}{E_i} \right) \\ &= \left[\sum_{k=1}^n s_{jk} (x_k + x_{n+k}) \right] \left[\sum_{i=1}^n \frac{1}{E_i} \right]. \end{aligned} \quad (23)$$

In (23), s_{jk} is a component of voltage eigenmatrix $[S_v]$ and x_k and x_{n+k} can be determined from (16). Note that (23) cannot be simply determined; thus, a computer program or symbolic software analysis package such as Mathematica or Maple can aid in this calculation.

III. GENERALIZED TRANSFER FUNCTION OF TWO COUPLED LINES

Reducing the matrix equation (9) with eigenmatrices and boundary conditions for two coupled lines, the transfer functions of two coupled lines in the frequency domain including frequency-dependent line parameters and linear uncoupled loading conditions, i.e., $Z_{G1} \neq Z_{G2} \neq Z_{L1} \neq Z_{L2}$, can be determined. The matrix of constants $[C_1]$ of two coupled lines in (9) is the magnitude of incident waves at the launching ports determined from boundary conditions

$$[C_1] = \begin{bmatrix} V_1^+(x=0) \\ V_2^+(x=0) \end{bmatrix}. \quad (24)$$

Similarly, the matrix of constants $[C_2]$ of two coupled lines is the magnitude of reflected waves at the launching ports determined from boundary conditions. Thus

$$[C_2] = \begin{bmatrix} V_1^-(x=0) \\ V_2^-(x=0) \end{bmatrix}. \quad (25)$$

Combining (9), (24), and (25), the two line transfer functions are given by

$$H_1(\omega) = \frac{1}{\det(A(\omega))} \{T_1(\omega) + T_2(\omega)\} \quad (26)$$

$$H_2(\omega) = \frac{1}{\det(A(\omega))} \{T_1(\omega) - T_2(\omega)\} \quad (27)$$

where H_1 and H_2 are transfer functions on line 1 (active line) and line 2 (quiet line), respectively, and

$$T_1(\omega) = \{\text{Cof}_{11}(A(\omega)) \exp(-\gamma_1(\omega)x) + \text{Cof}_{13}(A(\omega)) \exp(\gamma_1(\omega)x)\} \quad (28)$$

$$T_2(\omega) = \{\text{Cof}_{12}(A(\omega)) \exp(-\gamma_2(\omega)x) + \text{Cof}_{14}(A(\omega)) \exp(\gamma_2(\omega)x)\} \quad (29)$$

where $\text{Cof}_{ij}(A)$ is the cofactor of square matrix $[A]$ which is given by

$$[A(\omega)] \equiv \begin{bmatrix} A_1(\omega) \\ A_2(\omega) \\ A_3(\omega) \\ A_4(\omega) \end{bmatrix} \quad (30)$$

where the respective row vectors of matrix $[A]$ are given by

$$[A_1(\omega)] = \left(\left(\frac{Z_1(\omega) + Z_{G1}}{Z_1(\omega)} \right) \left(\frac{Z_2(\omega) + Z_{G1}}{Z_2(\omega)} \right) \right. \\ \left. \left(\frac{Z_1(\omega) - Z_{G1}}{Z_1(\omega)} \right) \left(\frac{Z_2(\omega) - Z_{G1}}{Z_2(\omega)} \right) \right)$$

$$[A_2(\omega)] = \left(\left(\frac{Z_1(\omega) + Z_{G2}}{Z_1(\omega)} \right) - \left(\frac{Z_2(\omega) + Z_{G2}}{Z_2(\omega)} \right) \right. \\ \left. \left(\frac{Z_1(\omega) - Z_{G2}}{Z_1(\omega)} \right) - \left(\frac{Z_2(\omega) - Z_{G2}}{Z_2(\omega)} \right) \right)$$

$$[A_3(\omega)] = \left(\left(\frac{Z_1(\omega) - Z_{L1}}{Z_1(\omega)} \right) \exp(-\gamma_1(\omega)l) \right. \\ \left(\frac{Z_2(\omega) - Z_{L1}}{Z_2(\omega)} \right) \exp(-\gamma_2(\omega)l) \\ \left(\frac{Z_1(\omega) + Z_{L1}}{Z_1(\omega)} \right) \exp(\gamma_1(\omega)l) \\ \left(\frac{Z_2(\omega) + Z_{L1}}{Z_2(\omega)} \right) \exp(\gamma_2(\omega)l) \right)$$

$$[A_4(\omega)] = \left(\left(\frac{Z_1(\omega) - Z_{L2}}{Z_1(\omega)} \right) \exp(-\gamma_1(\omega)l) \right. \\ - \left(\frac{Z_2(\omega) - Z_{L2}}{Z_2(\omega)} \right) \exp(-\gamma_2(\omega)l) \\ \left(\frac{Z_1(\omega) + Z_{L2}}{Z_1(\omega)} \right) \exp(\gamma_1(\omega)l) \\ - \left(\frac{Z_2(\omega) + Z_{L2}}{Z_2(\omega)} \right) \exp(\gamma_2(\omega)l) \right).$$

Solving the eigenvalue equation (7) in a two-line coupled system, the propagation constants of the lines are given by

$$\gamma_1 = \sqrt{(Y_{11} + Y_{12})(Z_{11} + Z_{12})} \quad (31)$$

$$\gamma_2 = \sqrt{(Y_{11} - Y_{12})(Z_{11} - Z_{12})} \quad (32)$$

where γ_1 and γ_2 are propagation constants of the eigenmodes. Similarly, the characteristic impedance matrix is defined as the ratio of the incident voltage wave to the reflected voltage wave. The characteristic impedance can be represented in terms of voltage eigenmatrix and current eigenmatrix as follows:

$$[Z] = [S_v][S_i]^{-1}. \quad (33)$$

Thus, the characteristic impedance matrix of two coupled lines is given by

$$[Z] = \frac{1}{2} \begin{bmatrix} Z_1 + Z_2 & Z_1 - Z_2 \\ Z_1 - Z_2 & Z_1 + Z_2 \end{bmatrix} \quad (34)$$

where

$$Z_1 = \sqrt{\frac{Z_{11} + Z_{12}}{Y_{11} + Y_{12}}} \quad \text{and} \quad Z_2 = \sqrt{\frac{Z_{11} - Z_{12}}{Y_{11} - Y_{12}}}. \quad (35)$$

Note that both the propagation constant and characteristic impedance matrices have been represented by the series impedance matrix of (3) and parallel admittance matrix of (4).

If $Z_{G1} = Z_{G2}$ and $Z_{L1} = Z_{L2}$ are assumed and the frequency-dependent components are replaced by constant parameters, this equation reduces to formulas in the literature [7], [8]

$$H_1 = \frac{1}{2}(T_1 + T_2) \quad (36)$$

$$H_2 = \frac{1}{2}(T_1 - T_2) \quad (37)$$

where

$$T_1 = \frac{Z_1}{Z_1 + Z_G} \frac{\exp(-\gamma_1 x) + \rho_{L1} \exp(-\gamma_1(2l - x))}{1 - \rho_{G1} \rho_{L1} \exp(-2\gamma_1 l)}$$

$$T_2 = \frac{Z_2}{Z_2 + Z_G} \frac{\exp(-\gamma_2 x) + \rho_{L2} \exp(-\gamma_2(2l - x))}{1 - \rho_{G2} \rho_{L2} \exp(-2\gamma_2 l)}$$

where

$$\rho_{L1} = \frac{Z_L - Z_1}{Z_L + Z_1}, \quad \rho_{G1} = \frac{Z_G - Z_1}{Z_G + Z_1},$$

$$\rho_{L2} = \frac{Z_L - Z_2}{Z_L + Z_2}, \quad \text{and} \quad \rho_{G2} = \frac{Z_G - Z_2}{Z_G + Z_2}.$$

With further assumptions, (36) and (37) can be represented in closed form in the time domain. Moreover, if impedance matching conditions are satisfied, then an even simpler form is produced [12]

$$H_i = \exp(-\gamma_i x). \quad (38)$$

This simplified form of the interconnect transfer function has been widely used in many applications at low frequencies or in carefully designed microwave circuits with matched generator and load resistances [12]–[14].

IV. SIMULATION ALGORITHM

Given this generalized multiconductor interconnect transfer function in the frequency domain, a method of time-domain simulation needed to be developed. For an input pulse function which is excited in IC interconnect, a very stable method of pulse decomposition was employed [15]. Using this pulse decomposition method, the pulse rise time and fall time can be approximated as the summation of delayed unit step functions. The input function is

$$f(t) = \sum_{i=1}^n f_i(t) \quad (39)$$

where

$$f_i(t) = \frac{A}{n} \left[U \left(t - \left(\frac{i-1}{n} \right) t_r \right) - U \left(t - \left(\frac{i-1}{n} \right) t_r - t_f \right) \right]. \quad (40)$$

Given the interconnect line circuit parameter matrices, the time-domain responses of the interconnect under generalized conditions can be obtained by inverse Fourier transform of (40)

$$V_i(t, x) = \mathcal{F}^{-1}[F_i(\omega)H_i(\omega, x)] \quad (41)$$

where i is the line number and x is the distance.

This generalized multiconductor transfer function and Fourier transform technique were used for simulation. A new algorithm (software) was developed for the simulations, and the simulations were verified with picosecond pulsed measurements of interconnect lines described in Section V. Using the generalized interconnect transfer function requires knowledge of the multiconductor interconnect line circuit

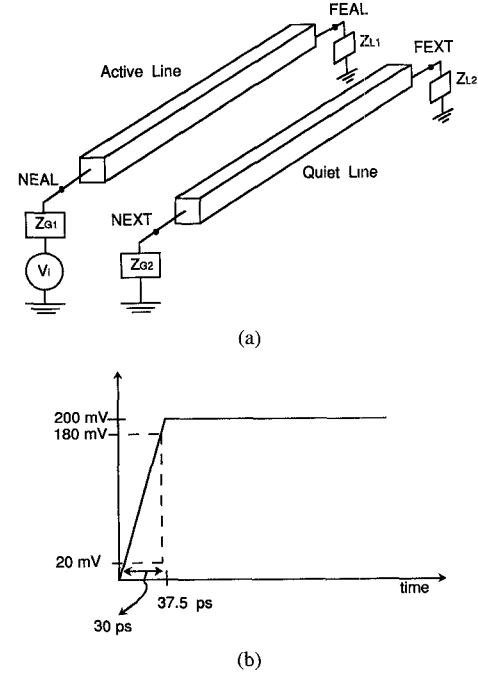


Fig. 2. (a) Two coupled interconnect circuit test points. (b) Input waveform of unit step pulse.

parameter matrices $[R]$, $[L]$, $[C]$, and $[G]$ and terminal conditions of interconnect circuits $[Z_G]$ and $[Z_L]$.

The frequency-dependent properties of the single conductor and coupled interconnect circuit models are reported [11], [16]. These model parameters are broken into three generic components. These are on-diagonal interconnect parameters (self-parameters), coupling capacitance parameters, and coupling inductance parameters. The on-diagonal parameters of the line circuit parameter matrix are calculated from the single-line interconnect frequency-dependent model [11]. The coupling capacitance can be calculated by PISCES-IIB simulation [17] or analytical models [18]–[20]. Then, the coupling inductance is found via the free space capacitance matrix [16].

V. SIMULATION AND EXPERIMENTAL VERIFICATION IN THE TIME DOMAIN

In an example of an coupled interconnect circuit, the signal test points are $V_1(t, 0)$, $V_2(t, 0)$, $V_1(t, l)$, and $V_2(t, l)$, as shown in Fig. 2. The acronyms used in Fig. 2 are, in order, signal at the near end of the activated line (NEAL), the near-end crosstalk (NEXT), signal at the far-end activated line (FEAL), and the far-end crosstalk (FEXT). These are the signal nodes in the interconnect circuit simulations. Fig. 3 shows test structure layouts and cross section.

Time-domain simulation shows signal transients, propagation, and crosstalk at the four coupled interconnect circuit test nodes with termination impedances as defined in Fig. 2. Given an input signal applied at V_i , signal transients can be observed in the four test points. Propagation delay and signal degradation can be observed in the test point FEAL. The NEXT and FEXT show crosstalk at the quiet line.

The coupled interconnect circuit simulations employ the generalized interconnect transfer function, i.e., (26) and (27),

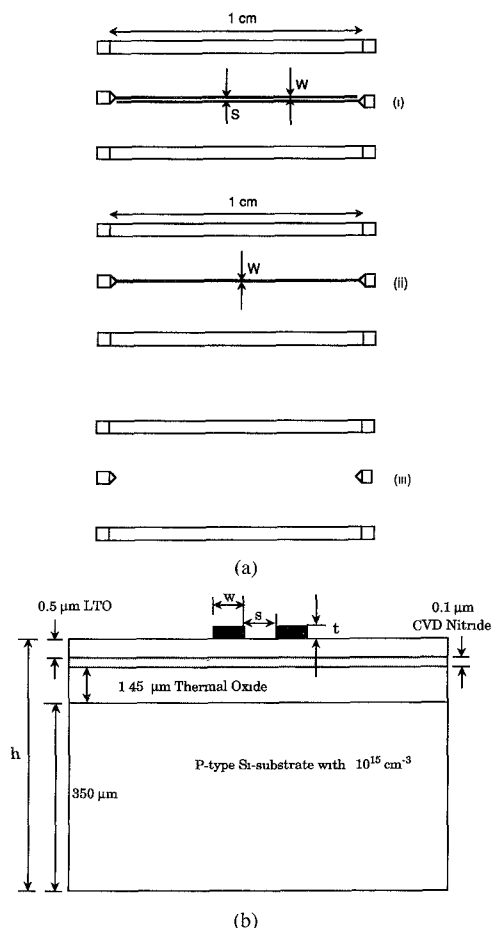


Fig. 3. (a) Cross-coupled interconnect test structure layout. (i) Cross-coupled test structure. (ii) Single line test structure. (iii) Open test structure. (b) Test structure cross-section.

and the frequency-dependent interconnect circuit parameter model (frequency-dependent RLCG model) [16]. These circuit simulations are verified using picosecond time-domain measurements.

The line spacing of the cross-coupled lines is $2\ \mu\text{m}$ and the line width is $4\ \mu\text{m}$. They are fabricated over an SiO_2 layer, a thin-film nitride layer, and a p-type Si-substrate with the concentration of $10^{15}\ \text{cm}^{-3}$. For the time-domain measurements, an HP54121T sampling oscilloscope is connected to the Cascade Microtech Probe Station and the DUT is mounted on the measurement chuck. The termination condition of the measurement system is $50\ \Omega$ at the measurement points.

Two-port network measurements make it possible to observe the FEXT from a cross-coupled test structure. Thus, if floating terminals of the DUT are assumed to have very high resistance ($1\ \text{M}\Omega$), then the simulation is easily done. Since the internally generated input function of the HP54121T sampling oscilloscope is a unit step with a 30 ps rise time [21], measurement results can be compared with the simulation results under the same conditions. As expected from simulation, the signal is delayed and dispersed, and there is crosstalk noise due to coupling.

Fig. 4(a) shows simulated reflected signal transients for near-end and far-end crosstalk of a quiet time (FEXT). Fig. 4(b) shows time-domain measurements of the reflected

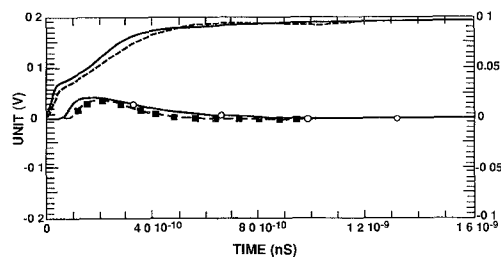


Fig. 4. Simulation and measurements of $4\text{-}\mu\text{m}$ linewidth and $2\text{-}\mu\text{m}$ line-spacing coupled-line response. Simulated reflected signal at NEAL (solid lines) and transmitted signal at FEXT (solid lines with circles). Measured reflection signal of the above coupled lines at NEAL (dashed lines) and transmitted signal at FEXT (dashed lines with boxes).

signal transients and crosstalk for the far end of the quiet line (FEXT). Similar results were obtained for all measured and simulated test points [16].

The simulation employed the frequency-variant RLCG model for diagonal element determination of transmission line circuit parameter matrices [11]. The comparison of the simulated and experimental results shows good agreement with experimental data. The narrow IC interconnect (4 and $2\ \mu\text{m}$) showed good agreement with measured data in magnitude, delay, and waveform. Wider IC interconnect ($10\ \mu\text{m}$) show good agreement in waveform and delay, but substantial magnitude error. The narrow interconnect is typically encountered in VLSI circuits. Sources of error include the simplified parameter extraction methodology, time-domain measurement error, and the pad parasitic response model.

This work shows that experimental measurements of interconnect signal transmission can be accurately estimated by the new generalized interconnect transfer function, that is, signal transient, crosstalk noise, and delay, which are essential information for IC circuit designs, can be predicted. The interconnect simulation can, with minor modifications, include high-frequency parasitic effects. These effects are hard to independently observe using existing time-domain experimental tools.

VI. SUMMARY AND DISCUSSION

Through an exact mathematical analysis, the generalized interconnect transfer function for coupled lines is derived. Using frequency-dependent parameter modeling of coupled interconnect, high-speed VLSI interconnect simulations have been performed. To verify these theoretical analyses, computer simulation and experiments in the time domain have been performed.

Test structures with cross-coupled lines were designed, and time-domain measurements were performed with a high-speed sampling oscilloscope, HP54121T. The experimental results showed good agreement with simulation results.

Today's high-speed circuit design faces very difficult and challenging modeling problems. Unlike low-frequency circuit designs, there are many sources of interconnect crosstalk (IC substrate coupling, capacitive coupling, inductive coupling, and package-related coupling) and three-dimensional parasitic effects. These are difficult to verify by simulation or experimental techniques. Indeed, the authors feel that char-

acterizing all performance aspects of the multilevel-metal network of signal transmission lines in high-speed VLSI circuits requires excessive computer resources or experimental resources. Models used for industrial design of VLSI circuits in SPICE or in VLSI timing simulators are highly simplified by necessity. There needs to be extensive experimental verification and determination of interconnect model error for high-performance digital circuit design. The error range of existing interconnect models can be investigated by comparing existing interconnect modeling with simulations based on the generalized interconnect transfer function. In addition, designers will need to use circuit simulation techniques such as SPICE and microwave circuit simulators on critical circuit paths in their circuit designs. The results in this paper can be used to simulate critical paths and subcircuits in IC designs.

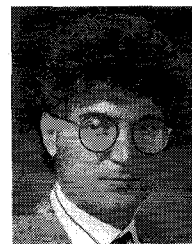
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